

## **Amendment to the Specification**

Page 4, second paragraph bridging to page 5:

An alternative method of using a wobble signal has been proposed in Japanese patent Laid-open No. 2001-209937 2001-2099237, whereby each of successive unit intervals of the wobble signal is modulated to have either a  $0^\circ$  or  $180^\circ$  phase, in accordance with whether the interval expresses the "1" or the "0" bit state. In the following, such phase modulation intervals of the wobble signal are referred to as unit data sections. During playback of the wobble signal, a sampling clock signal is generated which is locked in phase and frequency with the playback wobble signal, and which is used to generate sampling pulses for use in performing synchronous detection of the wobble signal, i.e., by sampling successive periods of that signal. The resultant sample values that are obtained for a unit data section are successively integrated, to obtain a final value (phase integration value), whose amplitude is indicative of the phase of the wobble signal within that unit data section. In that way, discrimination of the "1" and "0" bit states expressed in the playback wobble signal can be performed based on the levels of the phase integration values obtained for respective unit data sections.